

Appl. No. 09/808,097  
Amdt. dated June 24, 2003

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JUN 25 2003  
PATENT  
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**REMARKS**

Claims 1-2, 10, 13-17, and 19-99 were either previously or are currently canceled. Original claims 7-8 and previously amended claim 12 remain unchanged. Claims 3-6, 9, 11, and 18 are currently amended.

Claim 3 is amended to correct an inadvertent error, namely, "the second transistor" in line 10 of claim 3 is changed to "the first transistor". Claim 3 is further amended to remove the unnecessary limitation "after forming said main spacers," at line 11 of claim 3.

Claims 4 and 11 are similarly amended to remove the unnecessary limitations set forth in the second paragraph of each of claims 4 and 11, and to be made more clear by inserting "corresponding" before "highly doped region" at lines 9 and 10 of claims 4 and 11, respectively.

Claim 5 is amended at line 9 to insert "after forming said off-set spacers," before "performing a LDD implant", as suggested by the Examiner.

Claim 6 is amended to be made more clear by inserting "corresponding" before "highly doped region" at each of lines 7 and 9 of claim 6.

Claim 9 is amended to include substantially all the limitations of the canceled claim 10.

Claim 18 is amended to be made more clear.

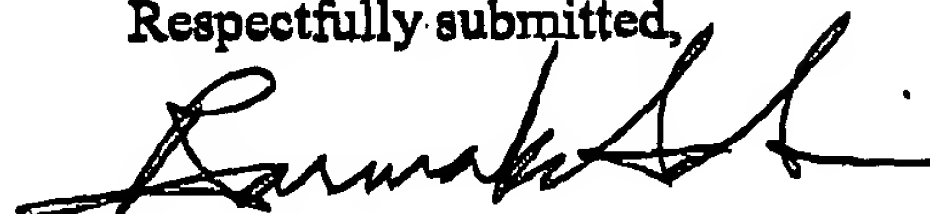
In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

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If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 650-326-2400.

Respectfully submitted,



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